

U.S. UTILITY Patent Application

O.I.P.E.

PATENT DATE

APP	LICATION NO. 09/@62597	CONT/PRIOR F	CLASS 324	SUBCLASS 772	ART UNIT 2858 2829	EXAMINER RUSSEll, K
APPLICANTS	Masahiro Taiichi	Kobayash Miya	i			and among the fourth

Resolver, resolver fault detection circuit, and resolver fault detection method $% \left(\frac{1}{2}\right) =0$

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ORIG N	AL	CROSS REFERENCE(S)						
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)					
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TERMINAL	DRAWINGS			CLAIMS ALLOWED		
DISCLAIMER	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.	
☐ The term of this patent	(Ássistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED		
subsequent to (date) has been disclaimed.						
The term of this patent shall not extend beyond the expiration date of U.S Patent. No.				ISSUE FEE		
				Amount Due	Date Paid	
• •	(Primary	Examiner)	(Date)			
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The terminalmonths of this patent have been disclaimed.	(Legal Instrum	nents Examiner)	(Date)			
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-om PTO-436A Rev. 6/99)		,	FILED WITH		FICHE CD-RC	